RapidIO over SpaceWire: Blending Complementary Protocols

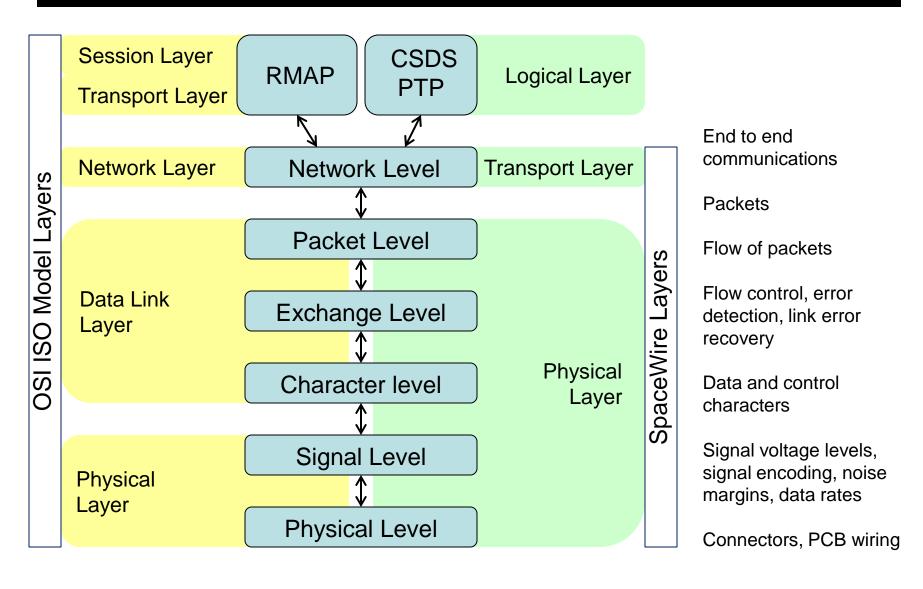
Networks and Protocols:

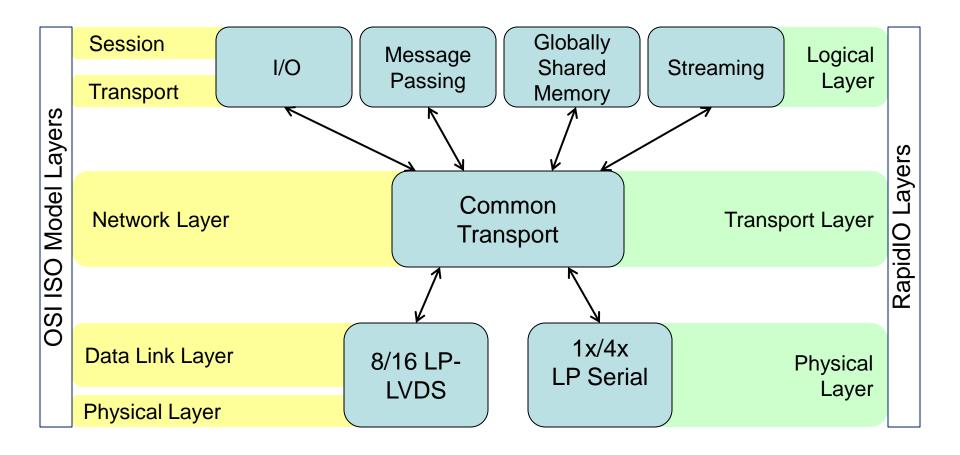
SpaceWire International Conference 2011

Steve Belvin
Systems Engineer

- Effective bandwidth utilization
 - Packet overhead should be low
- Effective bandwidth sharing
 - Limited packet size
 - Packet priority
- Varying payload sizes
 - Support various user services from I/O to streaming
- Low latency
 - Uncontrolled, deterministic or as low as possible
- Low power consumption

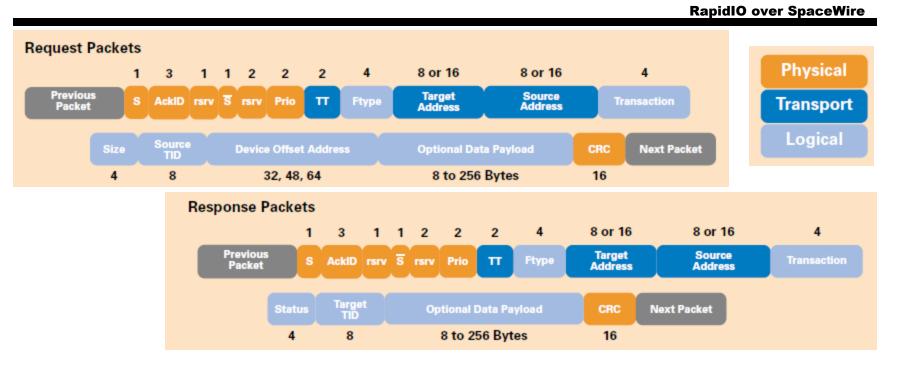
- Reliable data delivery
 - Varies from best effort to guaranteed delivery
- Multiple communications mechanisms
 - Support simple memorymapped reads and writes, multicast, event signaling, messaging, streaming
- Multiple topologies and sizes
- Standards based





- RapidIO is specified in three layers: Physical, Transport and Logical layers
- The Logical and Transport Layers are specified independent of the Physical Layer

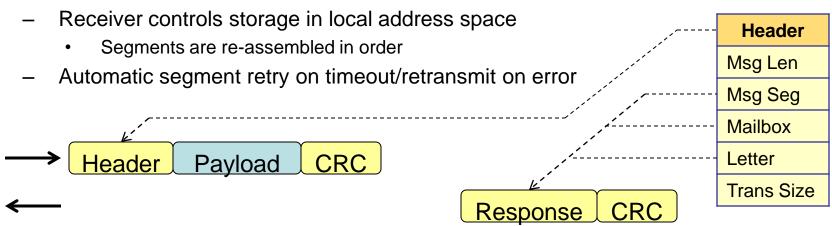
RapidIO Packet Format

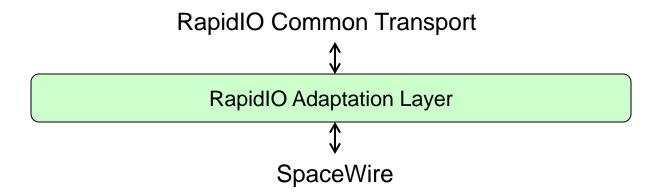


Logical & Transport Layers	
TT: Type of Transport	Size: Encoded transaction size
Target Address: Logical destination	Source TID: Transaction IDentity
Source Address: Logical source	Device Offset Address: Memory-mapped transactions
Ftype: Format Type	Payload
Transaction	Status: Result of transaction

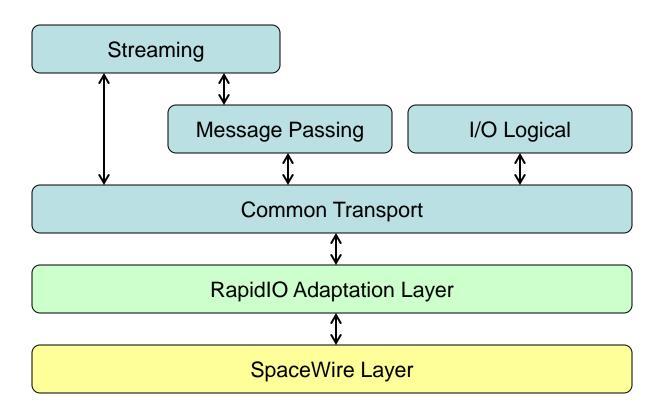
Physical Layer	
S: Packet or Control Symbol	
AckID: Acknowledge type	
PRIO: Packet priority for flow control	
CRC: 16-bit CRC for link error detection/retry	

- Two types of block data moves in distributed memory systems
 - 1. Direct memory access (DMA) source control
 - 2. Messaging target control
- Messaging commonly used in multiprocessor systems
 - "Share nothing" environment
 - In RapidIO, messaging handled using hardware messages and doorbells
- RapidIO messaging
 - Single message in flight from source to one mailbox/letterslot at the destination
 - Unique per priority level
 - Messages can be up to 4kbytes (maximum of 16 segments)

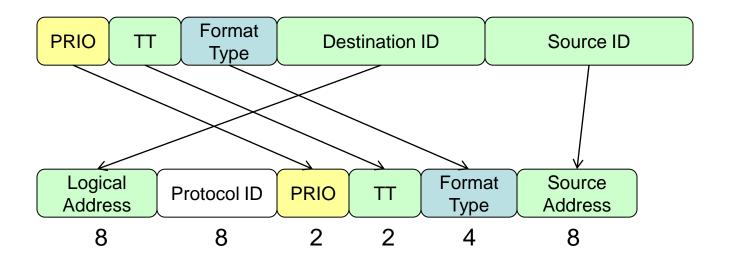




- RapidIO Adaptation Layer allows SpaceWire and RapidIO networks to be used together
- Operations of network adaptation layer
 - SpaceWire protocol identifier
 - Network address mapping
 - Packet delivery handshake
 - Packet integrity check
 - Packet and transaction delivery controller
 - End-to-end acknowledgement counter
 - Control and status registers added to RapidIO Configuration Space



- Message Passing provides end-to-end delivery guarantee over any other delivery guarantees provided by lower levels of stack
- Encapsulate Streaming in Message Passing when reliable delivery is needed



- Destination ID and Source ID fields are mapped to Logical Address
- Protocol ID is appended
- Priority, Transport Type and Format Type are added
- 8-bit Source and Destination are shown
 - Logical address size may be extended

- RapidIO Advantages
 - Low protocol overhead
 - Packet priority
 - In order delivery per flow
 - Link level handshaking
 - Standard configuration method
 - Support for multiple network services:
 I/O, Message Passing, Streaming,
 others
 - Message Passing: segmentation and re-assembly with reliable delivery
 - Use of commercial intellectual property for hardware and software
 - Extensions for specific applications
 - Future growth through commercial use

- RapidIO Disadvantages
 - Targeted at very high bandwidth applications
 - 1Gbps and up
 - Robust physical layer is costly (area and power)
 - Complex mixed signal transceiver
 - Link level handshaking

RapidIO's diverse set of upper layer protocols provides multiple services, including reliable delivery using Message Passing protocol combined with SpaceWire's lower transmission rates and lightweight Physical Layer results in an efficient and effective network for Space communications.

- The RapidIO Standard set of specifications provides many services desired in full-featured networks
 - The Common Transport Logical Layer Protocol is independent of the Physical Layer
 - Message Passing offers reliable delivery with segmentation and reassembly
- The SpaceWire Standard provides a low-gate count link to transport RapidIO Common Logical Layer Transport Layer Protocol information
- RapidIO software and hardware intellectual property may be blended with SpaceWire hardware intellectual property to form a fullfeatured network
 - Reduced physical layer area

Existing RapidIO products:

- IP from major FPGA vendors and others
 Embedded systems
- Semiconductor devices
- Boards and modules

- Test and measurement
- Software

- ECSS, "Space Engineering: SpaceWire Links, nodes, routers and networks", EECS-E50-12C, 31 July 2008.
- RapidIO Trade Organization, "RapidIO Interconnect Specification Part 6: LP-Serial Physical Layer Specification", Revision 2.2, June 2011.
- 3. ECSS, "Space Engineering: SpaceWire protocol identification", EECS-E50-51C, 5 Jan 2010.
- 4. ECSS, "Space Engineering: SpaceWire Remote memory access protocol", EECS-E50-52C, 5 Jan 2010.
- ECSS, "Space Engineering: SpaceWire CCSDS packet transfer protocol", EECS-E50-53C, 5 Jan 2010.
- 6. RapidIO Trade Organization, "RapidIO Interconnect Specification Part 3: Common Transport Layer Specification", Revision 2.2, June 2011.

- RapidIO Trade Organization, "RapidIO Interconnect Specification Part 1: Input/Output Logical Layer Specification", Revision 2.2, June 2011.
- 8. RapidIO Trade Association, "RapidIO, PCIExpress and Gigabit Ethernet Comparison: Pros and Cons of Using Interconnects in Embedded Systems", Revision 3, May, 2005.
- 9. RapidIO Trade Organization, "RapidIO Interconnect Specification Part 2: Message Passing Logical Layer Specification", Revision 2.2, June 2011.
- RapidIO Trade Organization, "RapidIO Interconnect Specification Part 10: Data Streaming Logical Layer Specification", Revision 2.2, June 2011.